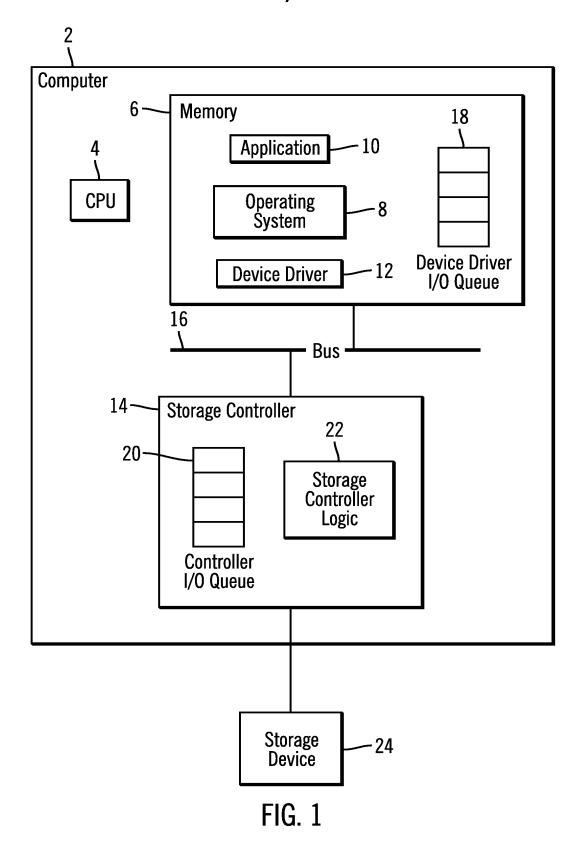
+

1/5



2/5

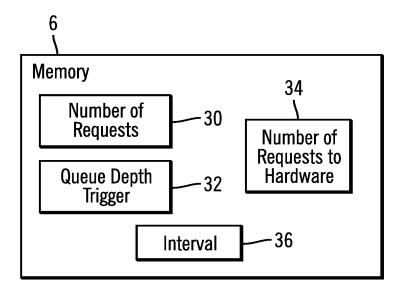
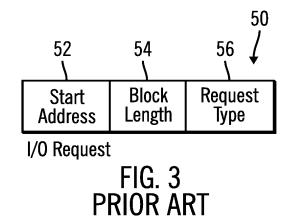


FIG. 2



4

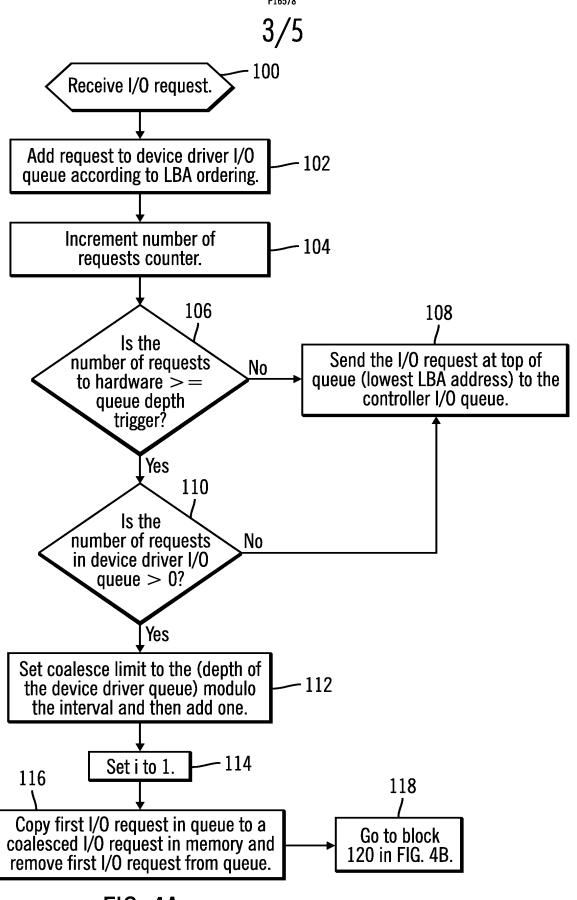


FIG. 4A



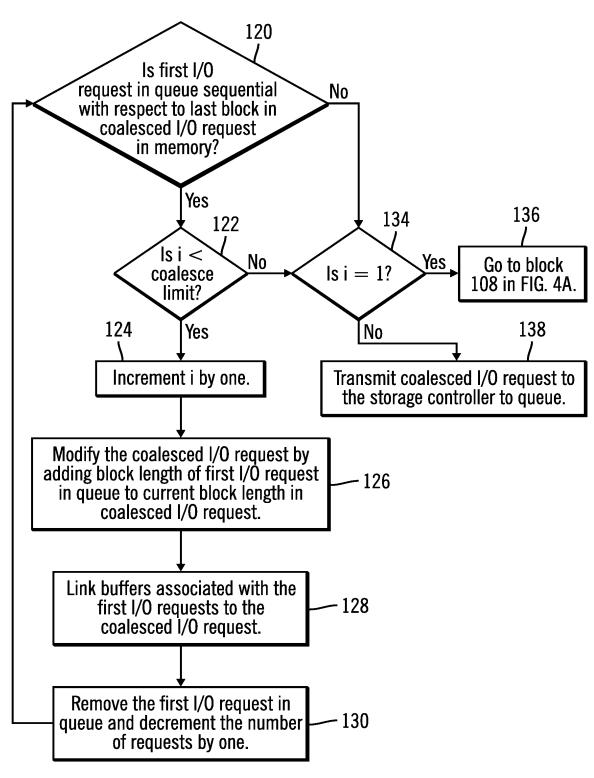


FIG. 4B

Number of Requests to Hardware > Queue Depth I/O Request Location Number of Requests Trigger **Coalesce Limit** 447 5 384 383 4 320 310 Device Driver I/O Queue Yes 3 256 255 2 192 191 1 128 127 Controller I/O Queue 64 No 63 0

 $\begin{array}{c} {\rm QueueDepthTrigger} = 127 \\ {\rm Interval} = 64 \end{array}$

FIG. 5